IN THE CLAIMS:

1. (Currently Amended) A method, ~~for supporting a determination of a correlation between at least one received code modulated signal and at least one available replica code, said method~~ comprising:

    storing signal samples of ~~said~~ at least one received code modulated signal with a first rate in a memory; ~~and~~

    reading stored signal samples with a second rate from said memory ~~for determining a correlation between said read signal samples and samples of said at least one available replica code~~, wherein said second rate is higher than said first rate;

    compensating in said read signal samples various possible Doppler frequencies; and

    determining for each of said possible Doppler frequencies a correlation between said compensated signal samples and samples of at least one available replica code.

2. (Currently Amended) The method ~~according to~~of claim 1, further comprising determining correlations between compensated signal samples and different replica codes based on signal samples that are read from said memory various times. ~~determining a correlation between said read signal samples and samples of said at least one available replica code.~~

3. (Currently Amended) The method ~~according to~~of claim 2, wherein determining a correlation comprises a coherent integration, and wherein said memory stores samples for up to at least one integration period of said coherent integration.

4. (Currently Amended) The method ~~according to~~of claim 3, wherein signal samples are only read from said memory when signal samples for at least one

integration period of said coherent integration have been stored in said memory.

5.   (Currently Amended) The method according to claim 2, wherein compensating in said read signal samples various possible Doppler frequencies ~~determining said correlation~~ comprises compensating in said read signal ~~extracted~~ samples sequentially various possible Doppler frequencies and wherein determining for each of said possible Doppler frequencies a correlation between said compensated signal samples and samples of at least one available replica code comprises applying sequentially for each of said possible Doppler frequencies a matched filter operation to said compensated signal samples.

6.   (Currently Amended) The method ~~according to~~of claim 5, wherein results of said matched filter operation are further subjected to at least one of a coherent integration and a non-coherent integration.

7.   (Currently Amended) An electronic device, comprising:

a memory component adapted to store signal samples of at least one received code modulated signal with a first rate;

a compensation component adapted to read signal samples stored in said memory with a second rate, said second rate being higher than said first rate, and to compensate in said read signal samples various possible Doppler frequencies; and

a correlation component ~~adapted to read signal samples stored in said memory with a second rate, which second rate is higher than said first rate, and~~ adapted to determine for each of said possible Doppler frequencies a correlation between said compensated ~~read~~ signal samples and samples of at least one available ~~a~~ replica code.

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8. (Currently Amended) The electronic device ~~according to~~of claim 7, wherein a determination of a correlation by said correlation component includes a coherent integration, and wherein said memory component is adapted to store signal samples for up to at least one integration period of said coherent integration.

9. (Currently Amended) The electronic device ~~according to~~of claim 8, wherein said correlation component is adapted to read signal samples from said memory component only when signal samples for at least one integration period of said coherent integration have been stored in said memory.

10. (Currently Amended) The electronic device ~~according to~~of claim 7, wherein said memory component is a sample memory component.

11. (Currently Amended) The electronic device ~~according to~~of claim 7, wherein said electronic device is a mobile terminal.

12. (Currently Amended) An apparatus, ~~chip for use in an electronic device, said chip~~ comprising:

a memory component configured ~~adapted~~ to store with a first rate signal samples of at least one received code modulated signal ~~received by said electronic device~~;

a compensation component configured to read signal samples stored in said memory with a second rate, said second rate being higher than said first rate, and to compensate in said read signal samples various possible Doppler frequencies; and

a correlation component ~~adapted to read signal samples stored in said memory with a second rate, which second rate is higher than said first rate, and adapted~~ configured to determine for each of said possible Doppler frequencies a correlation between said compensated ~~read~~ signal samples and samples of at least one available ~~a~~ replica code.

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13. (Currently Amended) The ~~apparatus~~ chip ~~according to~~of claim 12, wherein a determination of a correlation by said correlation component includes a coherent integration, and wherein said memory component is configured ~~adapted~~ to store signal samples for up to at least one integration period of said coherent integration.

14. (Currently Amended) The ~~apparatus~~ chip ~~according to~~of claim 13, wherein said correlation component is configured ~~adapted~~ to read signal samples from said memory component only when signal samples for at least one integration period of said coherent integration have been stored in said memory component.

15. (Currently Amended) The ~~apparatus~~ chip ~~according to~~of claim 12, wherein said memory is a sample memory.

16. (Currently Amended) A system including an electronic device and at least one network element of a communication network, said electronic device comprising:

a memory component adapted to store signal samples of at least one received code modulated signal at a first rate;

a compensation component adapted to read signal samples stored in said memory with a second rate, said second rate being higher than said first rate, and to compensate in said read signal samples various possible Doppler frequencies; and

a correlation component ~~adapted to read signal samples stored in said memory with a second rate, which second rate is higher than said first rate, and~~ adapted to determine for each of said possible Doppler frequencies a correlation between said compensated ~~read~~ signal samples and samples of at least one available ~~a~~ replica code.

17. (Currently Amended) The system ~~according to~~of claim 16, wherein a determination of a correlation by said correlation component includes a coherent integration, and wherein said memory component is adapted to store signal samples for up to at least one integration period of said coherent integration.

18. (Currently Amended) The system ~~according to~~of claim 16, further comprising at least one beacon transmitting said at least one code modulated signal.

19. (Currently Amended) The system ~~according to~~of claim 16, wherein said beacon is a satellite of a satellite based positioning system.

20. (Currently Amended) ~~A software program product in which a software code for supporting a determination of a correlation between at least one received code modulated signal and at least one available replica code is stored, said software code realizing the following steps when running in a processing unit~~A computer program product comprising a computer readable storage medium embodying computer program code thereon for execution by a computer processor, wherein said program code includes instructions for:

causing signal samples of at least one received code modulated signal to be stored with a first rate in a memory component; ~~and~~

causing signal samples stored in said memory component to be read from said memory component with a second rate ~~for determining a correlation between said read signal samples and samples of said at least one available replica code~~, wherein said second rate is higher than said first rate;

compensating in said read signal samples various possible Doppler frequencies; and

determining for each of said possible Doppler frequencies a correlation between said compensated signal samples and samples of at least one available replica code.

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21.   (New) The apparatus of claim 12, wherein said apparatus is a chip for use in
an electronic device.

22.   (New) An apparatus comprising:

     means for storing signal samples of at least one received code
modulated signal with a first rate in a memory;

     means for reading stored signal samples with a second rate from said
memory, wherein said second rate is higher than said first rate;

     means for compensating in said read signal samples various possible
Doppler frequencies; and

     means for determining for each of said possible Doppler frequencies a
correlation between said compensated signal samples and samples of at least
one available replica code.